

TOSHIBA Transistor Silicon PNP Epitaxial Type

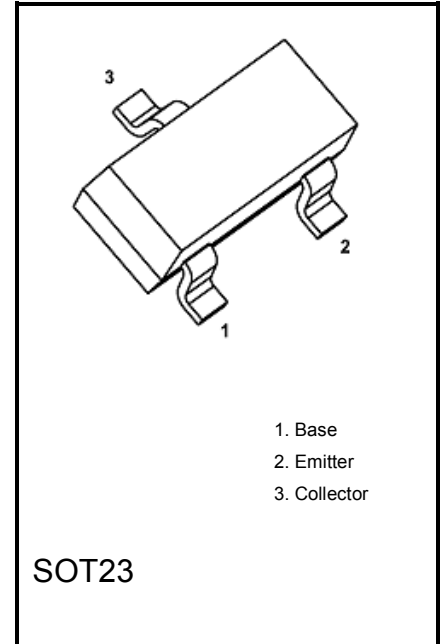
TMBT3906

Audio Frequency General Purpose Amplifier Applications

- High voltage and high current
: $V_{CEO} = -50\text{ V}$, $I_C = -200\text{ mA}$ (max)
- Complementary to TMBT3904

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-50	V
Collector-emitter voltage	V_{CEO}	-50	V
Emitter-base voltage	V_{EBO}	-5	V
Collector current	I_C	-200	mA
Base current	I_B	-30	mA
Collector power dissipation	PC (Note 1)	320	mW
	PC (Note 2)	1000	mW
Junction temperature	T_j	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55 to 150	$^\circ\text{C}$



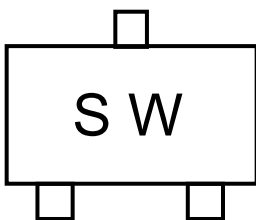
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on an FR4 board.
(25.4mm x 25.4mm x 1.6mm, Cu Pad: 0.42mm² x 3)

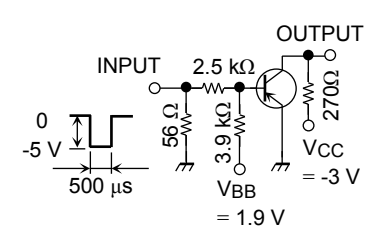
Note 2: Mounted on an FR4 board.
(25.4mm x 25.4mm x 1.6mm, Cu Pad: 645mm²)

Marking

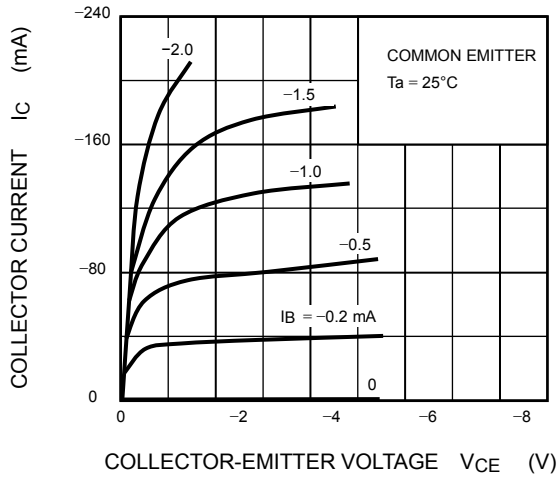


Start of commercial production
2015-01

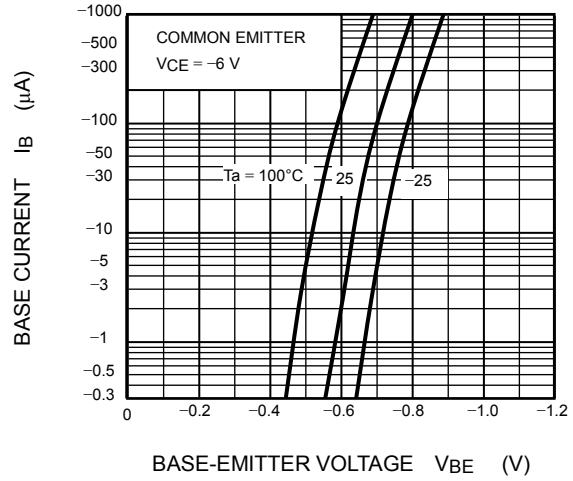
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		ICBO	V _{CB} = -50 V, I _E = 0 mA	—	—	-0.1	μA
Emitter cut-off current		IEBO	V _{EB} = -5 V, I _C = 0 mA	—	—	-0.1	μA
DC current gain		hFE	V _{CE} = -1 V, I _C = -0.1 mA	60	—	—	—
			V _{CE} = -1 V, I _C = -1 mA	80	—	—	
			V _{CE} = -1 V, I _C = -10 mA	100	—	300	
			V _{CE} = -1 V, I _C = -50 mA	60	—	—	
			V _{CE} = -1 V, I _C = -100 mA	30	—	—	
Collector-emitter saturation voltage		V _{CE (sat)}	I _C = -10 mA, I _B = -1 mA	—	—	-0.25	V
			I _C = -50 mA, I _B = -5 mA	—	—	-0.40	
Base-emitter saturation voltage		V _{BE (sat)}	I _C = -10 mA, I _B = -1 mA	—	—	-0.85	V
			I _C = -50 mA, I _B = -5 mA	—	—	-0.95	
Transition frequency		f _T	V _{CE} = -20 V, I _C = -10 mA	250	—	—	MHz
Collector output capacitance		C _{ob}	V _{CB} = -10 V, I _E = 0 mA, f = 1 MHz	—	4	7	pF
Noise figure		NF	V _{CE} = -5 V, I _C = -0.1 mA, f = 1 kHz, R _g = 1 kΩ,	—	—	4	dB
Switching times	delay time	t _d		—	—	35	ns
	rise time	t _r		—	—	35	
	storage time	t _s		—	—	200	
	fall time	t _f		—	—	50	
			I _C = -10mA, I _{B1} = -I _{B2} = -1mA				

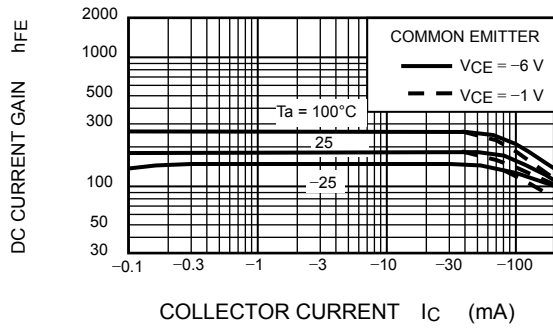
$I_C - V_{CE}$



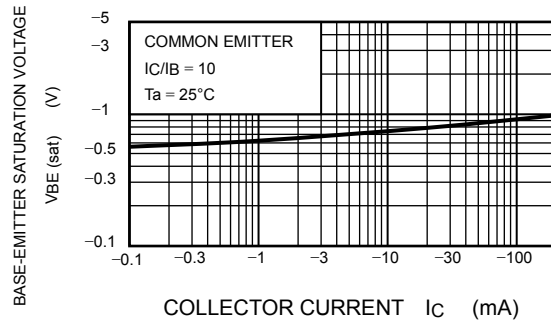
$I_B - V_{BE}$



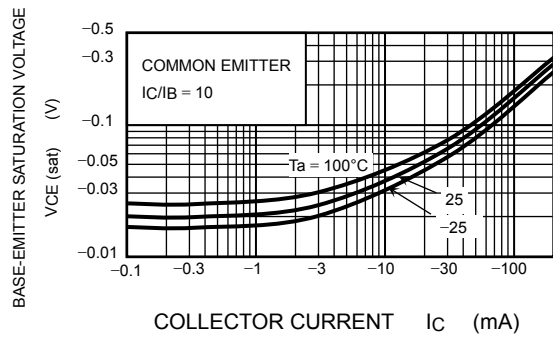
$h_{FE} - I_C$



$V_{BE}(\text{sat}) - I_C$



$V_{CE}(\text{sat}) - I_C$



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