

TCK207AN

0.75 V, 2A Load Switch IC with Reverse Current Blocking in Ultra Small Package

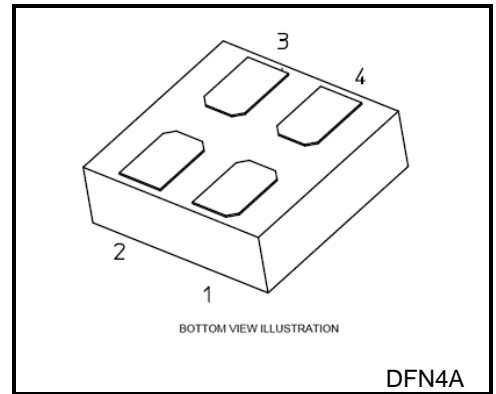
TCK207AN is 0.75 V ultra-low voltage load switch IC for a power management with slew rate control driver and reverse current blocking (SW OFF state) function.

Switch ON resistance is only 21.5 mΩ (typ.) at 0.75 V, -1.5 A condition and output current is available up to 2.0 A (DC). TCK207AN features output auto-discharge function.

This device is available in ultra-small package DFN4A (1.2 mm x 1.2 mm, t: 0.38 mm). Thus this device is ideal for portable applications that require high-density board assembly such as SSD, HDD, cellular phone.

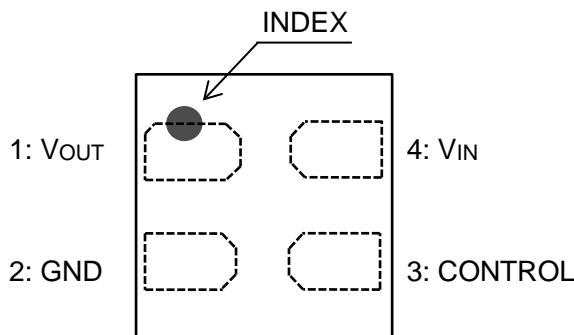
Features

- Low input voltage operation: $V_{IN} = 0.75$ to 3.6 V
- High output current: $I_{OUT} (DC) = 2.0$ A
- Low ON resistance :
 - $R_{ON} = 21.5$ mΩ (typ.) at $V_{IN} = 3.3$ V, -1.5 A
 - $R_{ON} = 21.5$ mΩ (typ.) at $V_{IN} = 1.2$ V, -1.5 A
 - $R_{ON} = 21.5$ mΩ (typ.) at $V_{IN} = 0.75$ V, -1.5 A
- Built in Slew rate control driver
- Built in Reverse current blocking(SW OFF state)
- Built in Auto-discharge
- Active High and Pull down connection between CONTROL and GND
- Ultra-small package : DFN4A (1.2 mm x 1.2 mm, t: 0.38 mm)

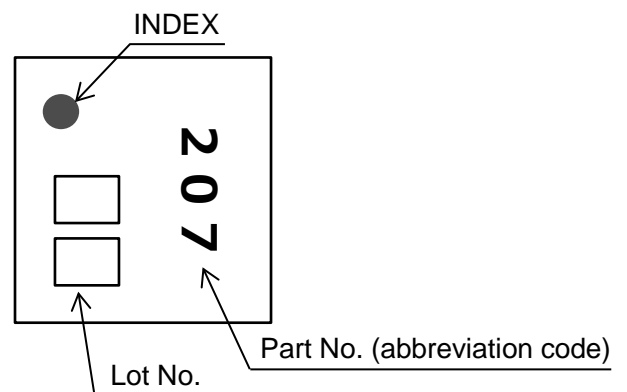


Weight : 1.4 mg (Typ.)

Pin Assignment (Top view)



Marking



Start of commercial production
2018-12

• Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating		Unit
Input voltage	V _{IN}	-0.3 to 4.0		V
Control voltage	V _{CT}	-0.3 to 4.0		V
Output voltage	V _{OUT}	-0.3 to V _{IN} +0.3		V
Output current	I _{OUT}	DC	2.0	A
		Pulse	3.0 (Note 1)	A
Power dissipation	P _D	1 (Note 2)		W
Operating temperature range	T _{opr}	-40 to 85		°C
Junction temperature	T _j	150		°C
Storage temperature	T _{stg}	-55 to 150		°C

Note : Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note1: 100 μs pulse, 2% duty cycle

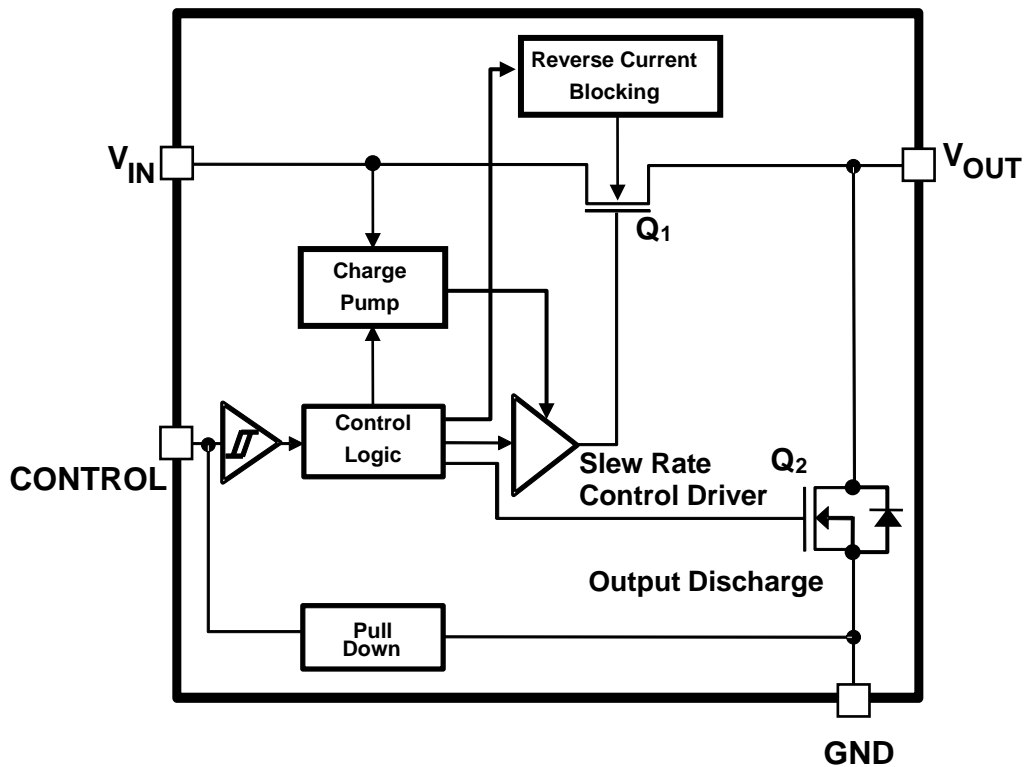
Note2: Rating at mounting on a board

(Glass epoxy(FR4) board dimension : 40mm x 40mm (double sided board), t=1.6mm

Metal pattern ratio : a surface approximately 70%, the reverse side approximately 70%

Through hole : diameter 0.5mm x 24)

- Block Diagram



- Function Table

Function		
Reverse current blocking	Output auto-discharge	Control pin connection
Built in	Built in	Pull down (Active High)

- Operation Logic Table

$0.75\text{ V} \leq V_{IN} \leq 3.6\text{ V}$ ($T_a = -40$ to 85°C)

Control voltage "High"	Output Q ₁	ON
	Discharge Q ₂	OFF
	Reverse current block	Disable
Control voltage "Low"	Output Q ₁	OFF
	Discharge Q ₂	ON
	Reverse current block	Active
Control voltage "OPEN"	Output Q ₁	OFF
	Discharge Q ₂	ON
	Reverse current block	Active

- Electrical Characteristics

- DC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	Ta = 25°C			Ta = -40 to 85°C		Unit	
			Min	Typ.	Max	Min	Max		
Input voltage	V _{IN}	—	0.75	—	3.6	0.75	3.6	V	
CONTROL High-level input voltage	V _{IH}	—	0.9 V ≤ V _{IN}	0.85	—	—	0.9 (Note3)	—	V
			V _{IN} < 0.9V	V _{IN}	—	—	V _{IN}	—	V
CONTROL Low-level input voltage	V _{IL}	—	0.9 V ≤ V _{IN}	—	—	0.4	—	0.4	V
			V _{IN} < 0.9V	—	—	0.3	—	0.3	V
Quiescent current (ON state)	I _Q	V _{IN} = V _{CT} = 3.6 V, I _{OUT} = 0 mA	—	22	—	—	35	μA	
Standby current (SW OFF state)	I _{Q(OFF)}	V _{IN} = 3.6 V, V _{CT} = 0 V, V _{OUT} = OPEN	—	0.7	—	—	5	μA	
Reverse blocking current	I _{RB}	V _{OUT} = 3.6 V, V _{IN} = 0 V, V _{CT} = 0 V	—	0.1	—	—	10	μA	
On resistance	R _{ON}	V _{IN} = 3.3 V, I _{OUT} = -1.5 A	—	21.5	31	—	37.3	mΩ	
		V _{IN} = 1.8 V, I _{OUT} = -1.5 A	—	21.5	31	—	37.3		
		V _{IN} = 1.2 V, I _{OUT} = -1.5 A	—	21.5	31	—	37.3		
		V _{IN} = 1.0 V, I _{OUT} = -1.5 A	—	21.5	31	—	37.3		
		V _{IN} = 0.75 V, I _{OUT} = -1.5 A	—	21.5	31	—	37.3		
Discharge on resistance	R _{SD}	V _{IN} = 1.8 V, V _{OUT} = 1.6 V	—	80	—	—	—	Ω	

Note 3: V_{IH} 0.85V (Min) @ Ta=0 to 85°C

- AC Characteristics (Ta = 25°C)

V_{IN} = 0.75 V

Characteristics	Symbol	Test Condition(Figure 1)	Min	Typ.	Max	Unit
V _{OUT} rise time	t _r	R _L =500Ω , C _L =0.1μF	—	180	—	μs
V _{OUT} fall time	t _f	R _L =500Ω , C _L =0.1μF	—	30	—	μs
Turn on delay	t _{ON}	R _L =500Ω , C _L =0.1μF	—	75	—	μs
Turn off delay	t _{OFF}	R _L =500Ω , C _L =0.1μF	—	8	—	μs

V_{IN} = 1.2 V

Characteristics	Symbol	Test Condition(Figure 1)	Min	Typ.	Max	Unit
V _{OUT} rise time	t _r	R _L =500Ω , C _L =0.1μF	—	240	—	μs
V _{OUT} fall time	t _f	R _L =500Ω , C _L =0.1μF	—	20	—	μs
Turn on delay	t _{ON}	R _L =500Ω , C _L =0.1μF	—	65	—	μs
Turn off delay	t _{OFF}	R _L =500Ω , C _L =0.1μF	—	8	—	μs

V_{IN} = 1.8 V

Characteristics	Symbol	Test Condition(Figure 1)	Min.	Typ.	Max.	Unit
V _{CT} - V _{OUT} rise time	t _{ON} + t _r	R _L =500Ω , C _L =0.1μF	—	400	—	μs

- AC Waveform

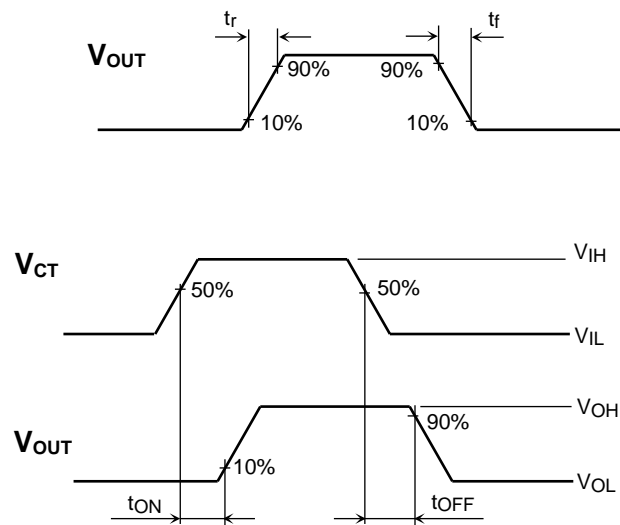
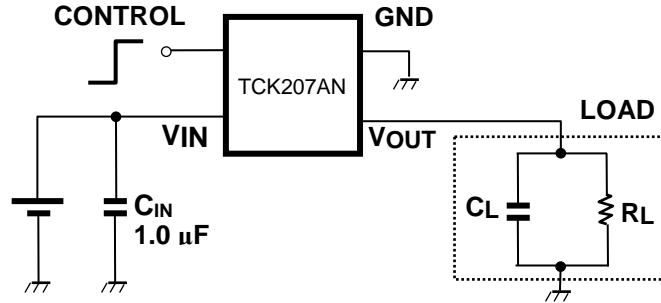


Figure 1 t_r, t_f, t_{ON}, t_{OFF} Waveforms

• Application Note

1. Application circuit example

The figure below shows the configuration for TCK207AN.



1) Input capacitor

An input capacitor (C_{IN}) is necessary for the stable operation of TCK207AN. And it is effective to reduce voltage drop due to sharp changes in output current and also for improved stability of the power supply. When used, place C_{IN} more than $1.0 \mu\text{F}$ as close to V_{IN} pin to improve stability of the power supply.

2) Output capacitor

An output capacitor (C_{OUT}) is not necessary for the guaranteed operation of TCK207AN. However, there is a possibility of overshoot or undershoot caused by output load transient response, board layout and parasitic components of load switch IC. In this case, an output capacitor with C_{OUT} more than $0.1 \mu\text{F}$ is recommended.

3) Control pin

A control pin for TCK207AN is Active High with Schmitt trigger circuit. This controls both the pass-through n-ch MOSFET and the discharge n-ch MOSFET, operated by the control voltage. When the control voltage level is High, Output n-ch MOSFET is ON state and discharge n-ch MOSFET is SW OFF state. When control voltage level is Low, the state of the MOSFETs is reversed. Also, pull down resistance equivalent to a few $\text{M}\Omega$ is connected between CONTROL and GND, thus the load switch IC is in SW OFF state even when CONTROL pin is OPEN. In addition, CONTROL pin has a tolerant function such that it can be used even if the control voltage is higher than the input voltage.

2. Reverse current blocking

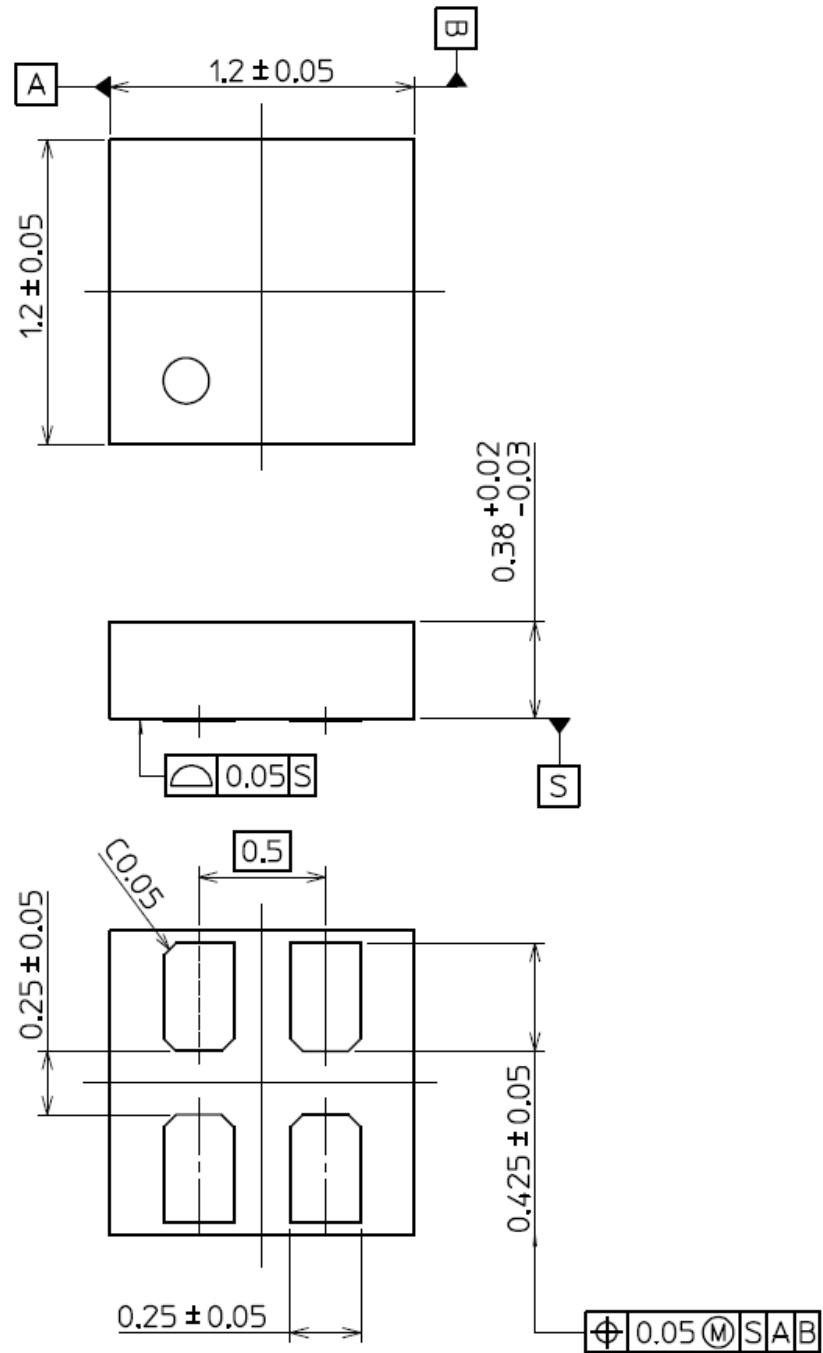
Reverse current blocking (SW OFF state) function is designed in this product. This function is active at output n-ch MOSEFT turned off.

However this function does not assure for the suppression of uprising device operation. In use of this product, please read through and understand dissipation idea for absolute maximum ratings from the above mention or our 'Semiconductor Reliability Handbook'. Then use this product under absolute maximum ratings in any condition. Furthermore, Toshiba recommends inserting failsafe system into the design.

- Package dimension

DFN4A

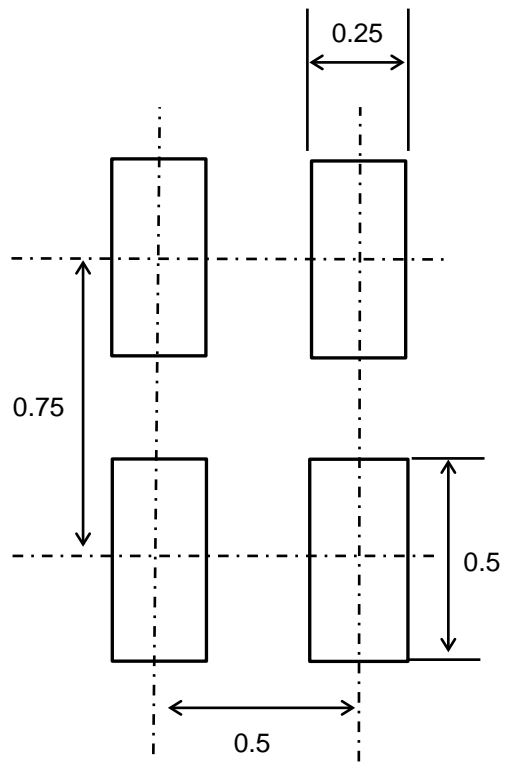
Unit: mm



Weight: 1.4 mg (Typ.)

- Land pattern dimensions (for reference only)

Unit: mm



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