Dear Customers,

March, 2019

Datasheet Correction: I2C function of TX/TLCS900 Family

This informs you that we found the following corrections should be made to the datasheets as shown below. If you have any questions or require any further information, please contact your local Toshiba representatives.

1. Products

M060 Group
M330 Group
M340 Group
M360 Group
M370 Group
M380 Group
M440 Group
M470 Group
TX19/A/H1 Series
TLCS900 Series

Note: For the detailed product list, refer to the item No.3.

2. Corrections

The function of I²C use are changed as following.

Note: This information explains the correction using the M360 Group product.
<Correction 1:  I²CxCR registger>

Registers Descriptions

[Old]

### 4.2.4. [I²CxCR2] (I²C control register 2)

<table>
<thead>
<tr>
<th>2</th>
<th>-</th>
<th>0</th>
<th>R</th>
<th>Description</th>
</tr>
</thead>
</table>
| 1:0 | SWRES[1:0] | 00 | W  | Software reset generation
  Write “10” followed by “01” to generate a reset.
  Writing “10” followed by “01” into this 2 bits generates a software reset (reset
  width is 1 clock as tsys).
  A software reset release the SCL and SDA lines (High state) if the device is
  data transfer and break the communication.
  The PC bus interface is initialized except [I²CxCR2]-I²CM and [I²CxDBR] register.
  When software reset is performed, make sure to write “0” to [I²CxCR2][7:4]. |

Note: the [I²CxCR2]-<MST>,<TRX>,<BB>,<PIN> bits are given independent functions, they are used in
typical combinations, as shown below, according to the [I²CxSR] setting.
When writing to these bits, make sure that notice.

[New]

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  When software reset is performed, make sure to write “0” to [I²CxCR2][7:4]. |

Note: the [I²CxCR2]-<MST>,<TRX>,<BB>,<PIN> bits are given independent functions, they are used in
typical combinations, as shown below, according to the [I²CxSR] setting.
When writing to these bits, make sure that notice.

**Note:** Don't change the contents of the registers, except [I²CxCR2]<SWRST[1:0]>, when the start condition is
generated, the stop condition is generated or the data transfer is in progress.
Write data to the registers before the start condition is generated or during the period from when an
interrupt request is generated for stopping the data transfer until it is released.
### 3. Product List

**M060 Group**
- TMPM061FW

**M330 Group**
- TMPM330FD
- TMPM333FY
- TMPM330FY
- TMPM330FW
- TMPM332FW
- TMPM332FW

**M340 Group**
- TMPM342FY
- TMPM343F10
- TMPM343FD

**M360 Group**
- TMPM365FY
- TMPM367FD
- TMPM366FW
- TMPM366FD
- TMPM366F20
- TMPM368FD
- TMPM369FD

**M370 Group**
- TMPM375FS
- TMPM376FD
- TMPM37AFS

**M380 Group**
- TMPM380FD
- TMPM383FS
- TMPM380FY
- TMPM384FD
- TMPM380FW
- TMPM381FW
- TMPM383FW

**M440 Group**
- TMPM440F10
- TMPM440FE

**M470 Group**
- TMPM470FD
- TMPM475FY
- TMPM470FZ
- TMPM470FY
- TMPM475FD
- TMPM475FZ

**TX19/A/H1 Series**
- All Products

**TLSC900 Series**
- All Products