

Dear Customers,

March, 2019

## **Datasheet Correction: I2C function of TX/TLCS900 Family**

This informs you that we found the following corrections should be made to the datasheets as shown below. If you have any questions or require any further information, please contact your local Toshiba representatives.

### **1. Products**

M060 Group  
M330 Group  
M340 Group  
M360 Group  
M370 Group  
M380 Group  
M440 Group  
M470 Group  
TX19/A/H1 Series  
TLCS900 Series

Note : For the detailed product list, refer to the item No.3 .

### **2. Corrections**

The function of I<sup>2</sup>C use are changed as following.

Note : This information explains the correction using the M360 Group product.

<Correction 1: I2CxCR register>

Registers Descriptions

[Old]

## 4.2.4. [I2CxCR2] (I<sup>2</sup>C control register 2)

2	-	0	R	Read as "0".
1:0	SWRES[1:0]	00	W	Software reset generation Write "10" followed by "01" to generate a reset. Writing "10" followed by "01" into this 2bits generates a software reset.(reset width is 1 clock as fsys) A software reset release the SCL and SDA lines (High state) if the device is data transfer and break the communication. The I <sup>2</sup> C bus interface is initialized except [I2CxCR2]<I2CM> and [I2CxDBR] register When software reset is performed, make sure to write "0" to [I2CxCR2][7:4]

Note: the [I2CxCR2]<MST>, <TRX>, <BB>, <PIN> bits are given independent functions, they are used in typical combinations, as shown below, according to the [I2CxSR] setting.  
When writing to these bits, make sure that notice.

[New]

## 4.2.4. [I2CxCR2] (I<sup>2</sup>C control register 2)

2	-	0	R	Read as "0".
1:0	SWRES[1:0]	00	W	Software reset generation Write "10" followed by "01" to generate a reset. Writing "10" followed by "01" into this 2bits generates a software reset.(reset width is 1 clock as fsys) A software reset release the SCL and SDA lines (HIGH state) if the device is data transfer and break the communication. The I <sup>2</sup> C bus interface is initialized except [I2CxCR2]<I2CM> and [I2CxDBR] register When software reset is performed, make sure to write "0" to [I2CxCR2][7:4]

Note: the [I2CxCR2]<MST>, <TRX>, <BB>, <PIN> bits are given independent functions, they are used in typical combinations, as shown below, according to the [I2CxSR] setting.  
When writing to these bits, make sure that notice.

Note: Don't change the contents of the registers, except [I2CxCR2]<SWRST[1:0]>, when the start condition is generated, the stop condition is generated or the data transfer is in progress.  
Write data to the registers before the start condition is generated or during the period from when an interrupt request is generated for stopping the data transfer until it is released.

## 3. Product List

### M060 Group

TMPM061FW

### M330 Group

TMPM330FD TMPM333FY	TMPM330FY	TMPM330FW	TMPM332FW	TMPM332FW
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### M340 Group

TMPM342FY	TMPM343F10	TMPM343FD
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### M360 Group

TMPM365FY TMPM367FD	TMPM366FW TMPM368FD	TMPM366FY TMPM369FD	TMPM366FD	TMPM366F20
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### M370 Group

TMPM375FS	TMPM376FD	TMPM37AFS
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### M380 Group

TMPM380FD TMPM383FS	TMPM380FY TMPM384FD	TMPM380FW	TMPM381FW	TMPM383FW
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### M440 Group

TMPM440F10	TMPM440FE
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### M470 Group

TMPM470FD TMPM475FY	TMPM470FZ	TMPM470FY	TMPM475FD	TMPM475FZ
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### TX19/ A/H1 Series Series

All Products

### TLSC900 Sereies

All Products