TOSHIBA Field-Effect Transistor Silicon N-Channel MOS Type (π-MOS V)

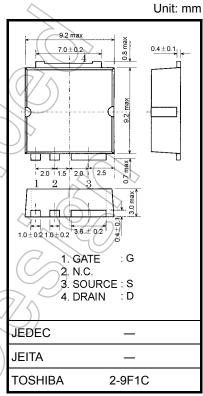
2SK3544

Switching Regulator Applications

- Low drain-source ON-resistance: $R_{DS (ON)} = 0.29 \Omega (typ.)$
- High forward transfer admittance: |Y_{fs}| = 5.8 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DSS} = 450 V)
- Enhancement mode: V_{th} = 3.0 to 5.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

| | | | | | \sim | |
|--|-------|----------|---------------------|------------|--------|--|
| Characteristics | | | Symbol | Rating | Unit | |
| Drain-source voltage | | | V_{DSS} | 450 | (VV) | |
| Drain–gate voltage ($R_{GS} = 20 \text{ k}\Omega$) | | | V_{DGR} | 450 | A | |
| Gate-source voltage | | | V _{GSS} | ±30 | V | |
| Drain current | DC | (Note 1) | ID | 13 | A | |
| | Pulse | (Note 1) | I _{DP} | 52 | A | |
| Drain power dissipation (Tc = 25°C) | | | PD | 100 | W | |
| Single-pulse avalanche energy (Note 2) | | | E _{AS} | 350 | (mJ | |
| Avalanche current | | | I _{AR} | 13 | A | |
| Repetitive avalanche energy (Note 3) | | | EAR | 4.5 | mJ | |
| Channel temperature | | | (T _{ch})) | 150 | //°C | |
| Storage temperature range | | | T _{stg} | –55 to 150 | Ç | |



Weight: 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

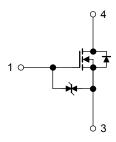
| Characteristics | Symbol | Max | Unit |
|-------------------------------------|------------|------|------|
| Thermal resistance, channel to case | Rth (ch-c) | 1.25 | °C/W |

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 3.46 mH, $R_G = 25 \Omega$, $I_{AR} = 13 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



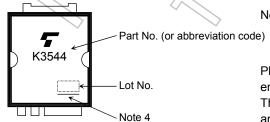
Electrical Characteristics (Ta = 25°C)

| Characteristics | | Symbol | Test Condition | Min | Тур. | Max | Unit |
|------------------------------|--------------------------------|----------------------|--|------------|------|----------|------|
| Gate leakage current Ic | | I _{GSS} | $V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | _ | ±10 | μА |
| Gate-source bre | akdown voltage | V (BR) GSS | $I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$ | ±30 | _ | _ | V |
| Drain cutoff curre | ent | I _{DSS} | V _{DS} = 450 V, V _{GS} = 0 V | / | _ | 100 | μΑ |
| Drain-source bre | Orain–source breakdown voltage | | $I_D = 10$ mA, $V_{GS} = 0$ V | 450 | _ | _ | V |
| Gate threshold voltage | | V _{th} | V _{DS} = 10 V, I _D = 1 mA | 3.0 |)/_ | 5.0 | V |
| Drain-source ON-resistance | | R _{DS} (ON) | V _{GS} = 10 V, I _D = 6 A |) <u> </u> | 0.29 | 0.4 | Ω |
| Forward transfer admittance | | Y _{fs} | V _{DS} = 10 V, I _D = 6 A | 3.0 | 5.8 | _ | S |
| Input capacitance | | C _{iss} | | | 1600 | _ | |
| Reverse transfer capacitance | | C _{rss} | V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz | _ | 17 | _ | pF |
| Output capacitance | | Coss | | _ | 220 | _ | |
| Switching time F | Rise time | t _r | V _{GS} D=6 A | - (| 28 | <u> </u> | ns |
| | Turn-on time | t _{on} | OV SRL = | | 45 |)_ | |
| | Fall time | t _f | 33.3 Ω V _{DD} ~ 200 V | 7 | 10 | _ | |
| | Turn-off time | t _{off} | Duty ≤ 1%, t _W = 10 μs |) _ | 56 | _ | |
| Total gate charge | | Qg | | _ | 34 | _ | |
| Gate-source charge | | Qgs | $V_{DD} \simeq 360 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$ | _ | 19 | _ | nC |
| Gate-drain charge | | Q _{gd} | | _ | 15 | _ | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|-----------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | | _ | | 13 | Α |
| Pulse drain reverse current (Note 1) | IDRP | | _ | | 52 | Α |
| Forward voltage (diode) | VDSF | I _{DR} = 13 A, V _{GS} = 0 V | _ | _ | -1.7 | ٧ |
| Reverse recovery time | t _{rr} | I _{DR} = 13 A, V _{GS} = 0 V, | _ | 300 | _ | ns |
| Reverse recovery charge | Qrr | dI _{DR} /dt = 100 A/μs | _ | 3.4 | _ | μС |

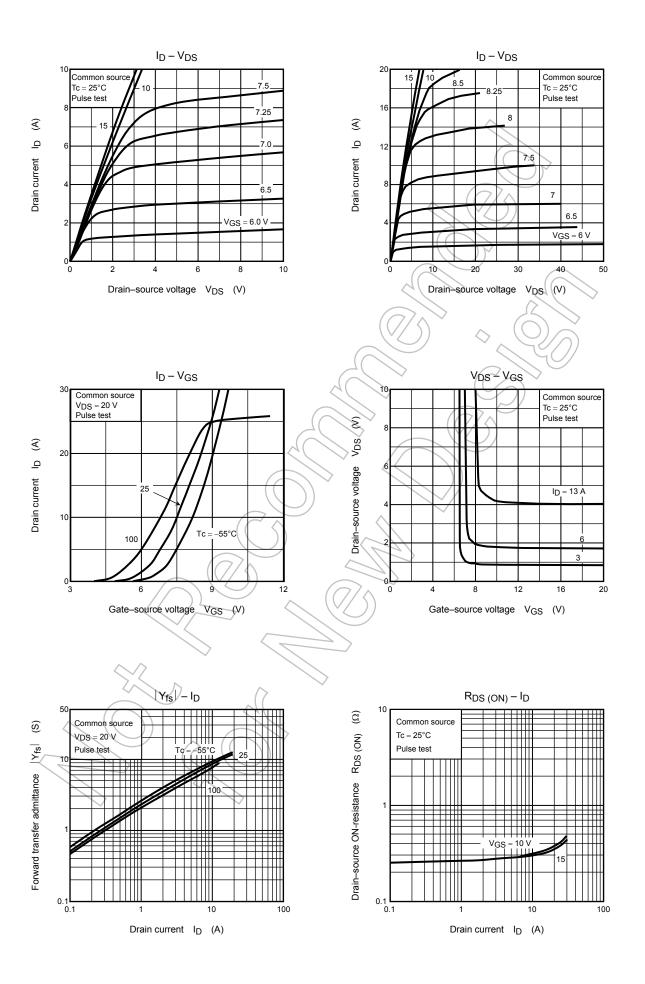
Marking

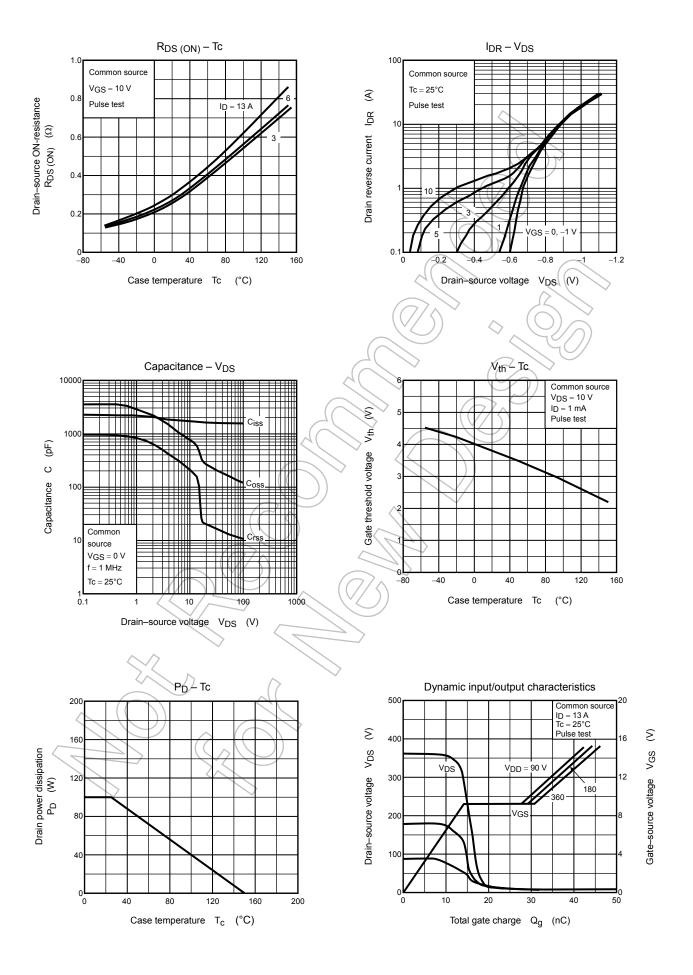


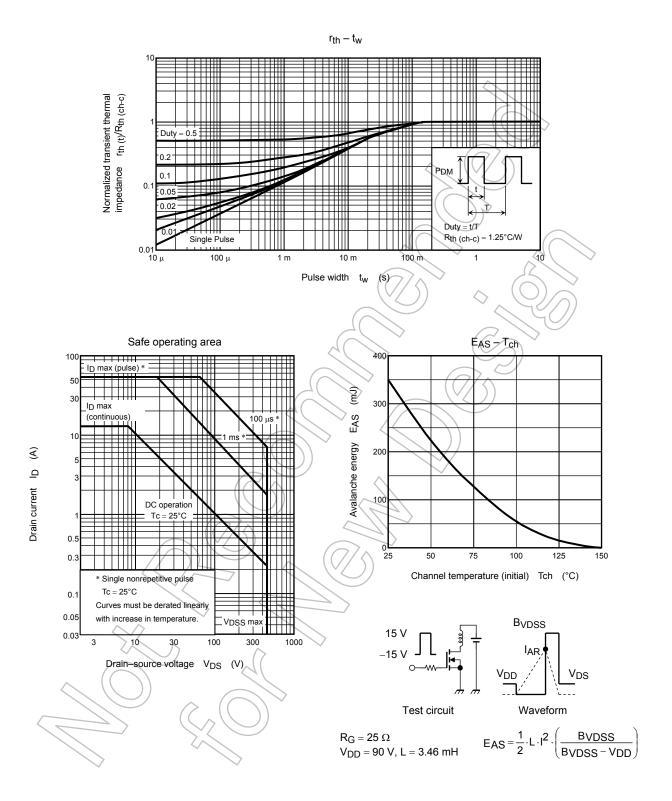
Note 4: A dot marking for identifying the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.







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