

CMOS Digital Integrated Circuits Silicon Monolithic

# TC74HCU04AFT

### 1. Functional Description

· Hex Inverter

#### 2. General

The TC74HCU04AFT is a high speed CMOS INVERTER fabricated with silicon gate C2MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

Since the internal circuit is composed of a single stage inverter, it can be used in analog applications such as crystal oscillators.

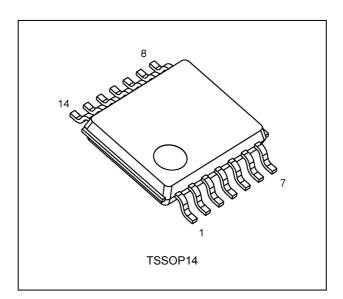
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### 3. Features

- (1) Wide operating temperature range:  $T_{opr} = -40$  to 125 °C (Note 1)
- (2) High speed:  $t_{pd} = 4 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- (3) Low power dissipation:  $I_{CC} = 1.0 \mu A \text{ (max) } T_a = 25 \text{ °C}$
- (4) High noise immunity:  $V_{NIH} = V_{NIL} = 10 \% V_{CC}$  (min)
- (5) Output drive capability: 10 LSTTL loads
- (6) Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 4 \text{ mA (min)}$
- (7) Balanced propagation delays:  $t_{PLH} \approx t_{PHL}$
- (8) Wide operating voltage range:  $V_{CC(opr)} = 2.0$  to 6.0 V
- (9) Pin and function compatible with 74LS04

Note 1: Operating Range spec of  $T_{opr}$  = -40 °C to 125 °C is applicable only for the products which manufactured after July 2020.

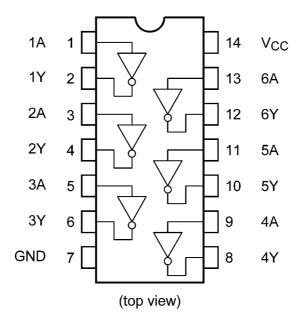
### 4. Packaging



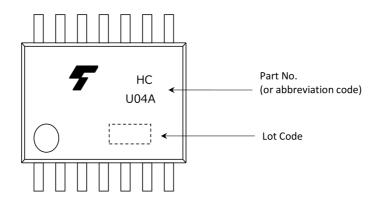
Start of commercial production



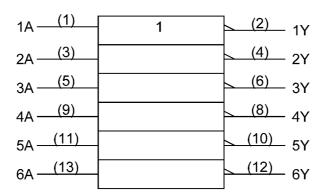
### 5. Pin Assignment



### 6. Marking



## 7. IEC Logic Symbol





#### 8. Truth Table

А	Y
L	Н
Н	L

### 9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 7.0	V
Input voltage	V <sub>IN</sub>		-0.5 to V <sub>CC</sub> + 0.5	V
Output voltage	V <sub>OUT</sub>		-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>		±20	mA
Output diode current	l <sub>ok</sub>		±20	mA
Output current	l <sub>out</sub>		±25	mA
V <sub>CC</sub> /ground current	I <sub>CC</sub>		±50	mA
Power dissipation	P <sub>D</sub>	(Note 1)	180	mW
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: 180 mW in the range of  $T_a$  = -40 to 85 °C. From  $T_a$  = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.

### 10. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		2.0 to 6.0	V
Input voltage	$V_{IN}$		0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>		0 to V <sub>CC</sub>	V
Operating temperature	$T_{opr}$	(Note 1)	-40 to 125	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 1: Operating Range spec of  $T_{opr}$  = -40 °C to 125 °C is applicable only for the products which manufactured after July 2020.



### 11. Electrical Characteristics

## 11.1. DC Characteristics (Unless otherwise specified, $T_a$ = 25 °C)

Characteristics	Symbol	Test Condition	ı	V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		2.0	1.7	_	_	V
				4.5	3.6	_	_	
				6.0	4.8	_	_	
Low-level input voltage	V <sub>IL</sub>	_		2.0	_	_	0.3	V
				4.5	_	_	0.9	
				6.0	_	_	1.2	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	2.0	1.8	2.0	_	V
				4.5	4.0	4.5	_	]
				6.0	5.5	5.9	_	
		V <sub>IN</sub> = GND	I <sub>OH</sub> = -4 mA	4.5	4.18	4.31	_	
			I <sub>OH</sub> = -5.2 mA	6.0	5.68	5.80	_	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH}$	I <sub>OL</sub> = 20 μA	2.0	_	0.0	0.2	V
				4.5	_	0.0	0.5	
				6.0	_	0.1	0.5	]
		V <sub>IN</sub> = V <sub>CC</sub>	I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	
			I <sub>OL</sub> = 5.2 mA	6.0	_	0.18	0.26	] <b> </b>
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	±0.1	μА
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		6.0	_	_	1.0	μΑ

## 11.2. DC Characteristics (Unless otherwise specified, $T_a$ = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		2.0	1.7	_	V
				4.5	3.6	_	
				6.0	4.8	_	
Low-level input voltage	V <sub>IL</sub>	_		2.0	_	0.3	V
				4.5	_	0.9	
				6.0	_	1.2	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	2.0	1.8	_	V
				4.5	4.0	_	
				6.0	5.5	_	
		V <sub>IN</sub> = GND	I <sub>OH</sub> = -4 mA	4.5	4.13	_	
			I <sub>OH</sub> = -5.2 mA	6.0	5.63	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 20 μA	2.0		0.2	V
				4.5	_	0.5	
				6.0	_	0.5	
		V <sub>IN</sub> = V <sub>CC</sub>	I <sub>OL</sub> = 4 mA	4.5	_	0.33	
			I <sub>OL</sub> = 5.2 mA	6.0	_	0.33	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND	•	6.0	_	±1.0	μА
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	10.0	μА



## 11.3. DC Characteristics (Note) (Unless otherwise specified, $T_a$ = -40 to 125 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		2.0	1.7	_	V
				4.5	3.6	_	
				6.0	4.8	_	
Low-level input voltage	V <sub>IL</sub>	_		2.0	ı	0.3	\ \
				4.5		0.9	
				6.0	_	1.2	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	2.0	1.8	_	V
				4.5	4.0	_	
				6.0	5.5	_	
			I <sub>OH</sub> = -4 mA	4.5	3.7	_	
			I <sub>OH</sub> = -5.2 mA	6.0	5.2	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 20 μA	2.0	_	0.2	V
				4.5	_	0.5	
				6.0	_	0.5	
			I <sub>OL</sub> = 4 mA	4.5	_	0.4	]
			I <sub>OL</sub> = 5.2 mA	6.0	_	0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	±1.0	μА
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	20.0	μА

Note: Operating Range spec of  $T_{opr}$  = -40 °C to 125 °C is applicable only for the products which manufactured after July 2020.



### 11.4. AC Characteristics (Unless otherwise specified, $C_L = 15$ pF, $V_{CC} = 5$ V, $T_a = 25$ °C, Input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> ,t <sub>THL</sub>	_	_	4	8	ns
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	_	_	4	8	ns

### 11.5. AC Characteristics (Unless otherwise specified, $C_L = 50$ pF, $T_a = 25$ °C, Input: $t_f = t_f = 6$ ns)

Characteristics	Symbol	Note	V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> ,t <sub>THL</sub>		2.0	_	30	75	ns
			4.5	_	8	15	
			6.0	_	7	13	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		2.0	_	18	60	ns
			4.5	_	6	12	
			6.0	_	5	10	
Input capacitance	C <sub>IN</sub>		_	_	9	15	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)	_		13	_	pF

Note 1: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.  $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/6$  (per gate)

### 11.6. AC Characteristics (Unless otherwise specified, $C_L = 50$ pF, $T_a = -40$ to 85 °C, Input: $t_f = t_f = 6$ ns)

Characteristics	Symbol	V <sub>CC</sub> (V)	Min	Max	Unit
Output transition time	t <sub>TLH</sub> ,t <sub>THL</sub>	2.0	_	95	ns
		4.5	_	19	
		6.0	_	16	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	2.0	_	75	ns
		4.5	_	15	
		6.0	_	13	
Input capacitance	C <sub>IN</sub>	1	_	15	pF

### 11.7. AC Characteristics (Note) (Unless otherwise specified, $C_L = 50$ pF, $T_a = -40$ to 125 °C, Input: $t_f = t_f = 6$ ns)

Characteristics	Symbol	V <sub>CC</sub> (V)	Min	Max	Unit
Output transition time	t <sub>TLH</sub> ,t <sub>THL</sub>	2.0	_	110	ns
		4.5	_	22	
		6.0	_	18	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	2.0	_	85	ns
		4.5	_	17	
		6.0	_	15	
Input capacitance	C <sub>IN</sub>	_	_	15	pF

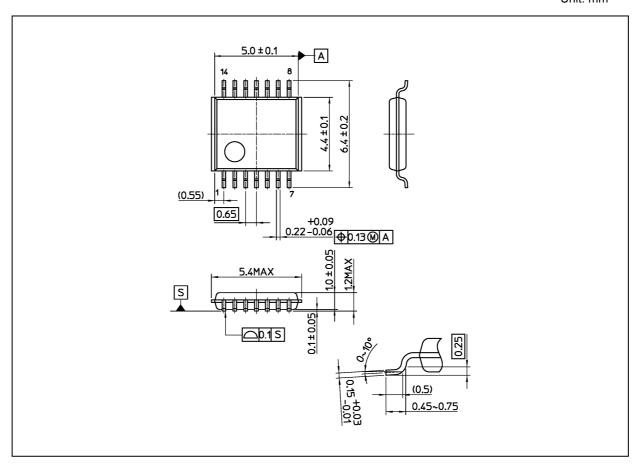
Note: Operating Range spec of T<sub>opr</sub> = -40 °C to 125 °C is applicable only for the products which manufactured after July 2020.

Rev.2.0



### **Package Dimensions**

Unit: mm



Weight: 0.06 g (typ.)

Package Name(s)
Nickname: TSSOP14



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