

CMOS Digital Integrated Circuits Silicon Monolithic

TC74LCX16245

1. Functional Description

· Low-Voltage 16-Bit Bus Transceiver with 5-V Tolerant Inputs and Outputs

2. General

The TC74LCX16245 is a high-performance CMOS 16-bit bus transceiver. Designed for use in 2.5 V or 3.3 V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (2.5 V or 3.3 V) V_{CC} applications, but it could be used to interface to 5 V supply environment for both inputs and outputs.

This 16-bit bus transceiver is controlled by direction control (DIR) inputs and output enable (\overline{OE}) inputs which are common to each byte. It can be used as two 8-bit transceiver or one 16-bit transceiver. The direction of data transmission is determined by the level of the DIR inputs. The \overline{OE} inputs can be used to disable the device so that the busses are effectively isolated.

All inputs are equipped with protection circuits against static discharge.

3. Features (Note)

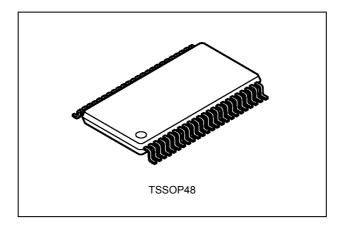
- (1) Wide operating temperature range: $T_{opr} = -40$ to 125 °C (Note 1)
- (2) Low-voltage operation: $V_{CC} = 2.0$ to 3.6 V
- (3) High-speed operation: $t_{pd} = 4.5 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- (4) Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- (5) Package: TSSOP
- (6) Bidirectional interface between 5.0 V and low-voltage (2.5 V or 3.3 V) signals
- (7) Power-down protection provided on all inputs and outputs

Note: Do not apply a signal to any bus pins when it is in the output mode. Damage may result.

All floating (high impedance) bus pins must have their input levels fixed by means of pull-up or pull-down resistors.

Note 1: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

4. Packaging



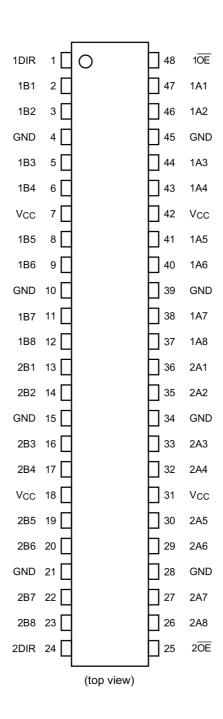
1

Start of commercial production

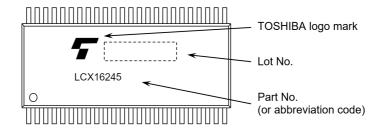
Rev.1.0



5. Pin Assignment



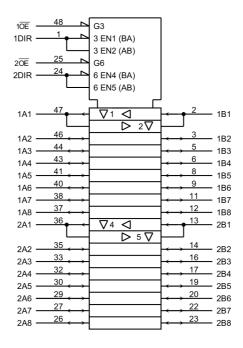
6. Marking



Rev.1.0



7. IEC Logic Symbol



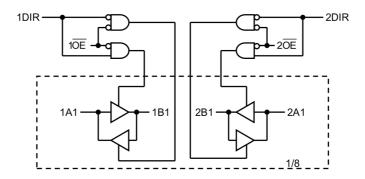
8. Truth Table

Inputs 1 <u>OE</u> 2OE	Inputs 1DIR 2DIR	Outputs	Function Bus 1A1-1A8 Bus 2A1-2A8	Function Bus 1B1-1B8 Bus 2B1-2B8
L	L	A = B	Output	Input
L	Н	B = A	Input	Output
Н	Х	Z	Z	Z

X: Don't care

Z: High impedance

9. System Diagram





10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 6.0	V
Input voltage (DIR/OE)	V _{IN}		-0.5 to 7.0	٧
Bus I/O voltage	V _{I/O}	(Note 1)	-0.5 to 7.0	V
		(Note 2)	-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-50	mA
Output diode current	I _{OK}	(Note 3)	±50	mA
Output current	I _{OUT}		±50	mA
Power dissipation	P _D	(Note 4)	400	mW
V _{CC} /ground current (per supply pin)	I _{CC} /I _{GND}		±100	mA
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Output in OFF state.
- Note 2: High (H) or Low (L) state. I_{OUT} absolute maximum rating must be observed.
- Note 3: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$
- Note 4: 400 mW in the range of Ta = -40 to 85 $^{\circ}$ C. From Ta = 85 to 125 $^{\circ}$ C a derating factor of -6.25 mW/ $^{\circ}$ C shall be applied until 150 mW.

11. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V_{CC}		2.0 to 3.6	V
		(Note 1)	1.5 to 3.6	
Input voltage (DIR/OE)	V _{IN}		0 to 5.5	V
Bus I/O voltage	V _{I/O}	(Note 2)	0 to 5.5	V
		(Note 3)	0 to V _{CC}	
Output current	I _{OH} ,I _{OL}	(Note 4)	±24	mA
		(Note 5)	±12	
		(Note 6)	±8	
Operating temperature	T _{opr}	(Note 7)	-40 to 125	°C
Input rise and fall times	dt/dv	(Note 8)	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs and bus inputs must be tied to either V_{CC} or GND. Please connect both bus inputs and the bus outputs with V_{CC} or GND when the I/O of the bus terminal changes by the function. In this case, please note that the output is not short-circuited.

- Note 1: Data retention only.
- Note 2: Output in OFF state.
- Note 3: High (H) or Low (L) state.
- Note 4: V_{CC} = 3.0 to 3.6 V
- Note 5: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$
- Note 6: V_{CC} = 2.3 to 2.7 V
- Note 7: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.
- Note 8: V_{IN} = 0.8 to 2.0 V , V_{CC} = 3.0 V



12. Electrical Characteristics

12.1. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		2.3 to 2.7	1.7	_	V
				2.7 to 3.6	2.0	_	
Low-level input voltage	V _{IL}	_		2.3 to 2.7	_	0.7	V
				2.7 to 3.6		0.8	
High-level output voltage	V _{OH}	$V_{IN} = V_{IH}$ or V_{IL}	I _{OH} = -100 μA	2.3 to 3.6	V _{CC} - 0.2	_	V
			I_{OH} = -8 mA	2.3	1.8	_	
			I _{OH} = -12 mA	2.7	2.2	_	
			I_{OH} = -18 mA	3.0	2.4		
			I _{OH} = -24 mA	3.0	2.2	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	2.3 to 3.6	_	0.2	V
			I _{OL} = 8 mA	2.3	_	0.6	
			I _{OL} = 12 mA	2.7	_	0.4	
			I _{OL} = 16 mA	3.0	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 0 to 5.5 V		2.3 to 3.6	_	±5.0	μΑ
3-state output OFF-state leakage current	I _{OZ}	$V_{IN} = V_{IH}$ or V_{IL} $V_{OUT} = 0$ to 5.5 V		2.3 to 3.6		±5.0	μА
Power-OFF leakage current	I _{OFF}	$V_{IN}/V_{OUT} = 5.5 V$		0		10.0	μΑ
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		2.3 to 3.6		20.0	μΑ
		V _{IN} /V _{OUT} = 3.6 to 5.5 V		2.3 to 3.6	_	±20.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per input)		2.3 to 3.6	_	500	μА



12.2. DC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition	n	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	_		2.3 to 2.7	1.7	_	V
				2.7 to 3.6	2.0		
Low-level input voltage	V _{IL}	_		2.3 to 2.7		0.7	V
				2.7 to 3.6	_	0.8	
High-level output voltage	V _{OH}	$V_{IN} = V_{IH}$ or V_{IL}	I _{OH} = -100 μA	2.3 to 3.6	V _{CC} - 0.2	_	V
			I_{OH} = -8 mA	2.3	1.55	_	
			I _{OH} = -12 mA	2.7	2.0	-	
			I _{OH} = -18 mA	3.0	2.2	-	
			I _{OH} = -24 mA	3.0	1.9	_	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	2.3 to 3.6	_	0.2	V
			I _{OL} = 8 mA	2.3		0.9	
			I _{OL} = 12 mA	2.7	ı	0.6	
			I _{OL} = 16 mA	3.0	ı	0.6	
			I _{OL} = 24 mA	3.0		0.8	
Input leakage current	I _{IN}	V _{IN} = 0 to 5.5 V		2.3 to 3.6		±20.0	μΑ
3-state output OFF-state leakage current	l _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \text{ to } 5.5 \text{ V}$		2.3 to 3.6		±20.0	μА
Power-OFF leakage current	I _{OFF}	V _{IN} /V _{OUT} = 5.5 V		0		40.0	μА
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		2.3 to 3.6		80.0	μА
		$V_{IN}/V_{OUT} = 3.6 \text{ to } 5.5 \text{ V}$		2.3 to 3.6		±80.0	
	Δl _{CC}	V _{IH} = V _{CC} - 0.6 V (per input)		2.3 to 3.6		5000	μА

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

12.3. AC Characteristics (Unless otherwise specified, T_a = -40 to 85°C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	2.5 ± 0.2	30	1.5	5.4	ns
			Table 12.7.1, Fig. 12.8.1, Table 12.8.1	2.7	50	1.5	5.2	
			Table 12.0.1	3.3 ± 0.3	50	1.5	4.5	
3-state output enable time	t _{PZL} ,t _{PZH}		See 12.7 AC Test Circuit,		30	1.5	8.5	ns
			Table 12.7.1, Fig. 12.8.2, Table 12.8.1	2.7	50	1.5	7.2	
			Table 12.0.1	3.3 ± 0.3	50	1.5	6.5	
3-state output disable	t_{PLZ}, t_{PHZ}		See 12.7 AC Test Circuit,	2.5 ± 0.2	30	1.5	7.7	ns
time			Table 12.7.1, Fig. 12.8.2, Table 12.8.1	2.7	50	1.5	6.9	
			Table 12.6.1		50	1.5	6.0	
Output skew	t _{osLH} ,t _{osHL}	(Note 1)	_	2.5 ± 0.2	30	_	_	ns
				2.7	50	_	_	
				3.3 ± 0.3	50		1.0	

Note 1: Parameter guaranteed by design. ($t_{osLH} = |t_{PLH}m - t_{PLH}n|$, $t_{osHL} = |t_{PHL}m - t_{PHL}n|$)



12.4. AC Characteristics (Note) (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}		See 12.7 AC Test Circuit,	2.5 ± 0.2	30	1.5	5.9	ns
			Table 12.7.1, Fig. 12.8.1, Table 12.8.1	2.7	50	1.5	5.7	
			Table 12.0.1	3.3 ± 0.3	50	1.5	4.9	
3-state output enable time	t_{PZL}, t_{PZH}		See 12.7 AC Test Circuit,	2.5 ± 0.2	30	1.5	9.4	ns
			Table 12.7.1, Fig. 12.8.2, Table 12.8.1	2.7	50	1.5	8.0	
			Table 12.6.1		50	1.5	7.2	
3-state output disable	t_{PLZ}, t_{PHZ}		See 12.7 AC Test Circuit,	2.5 ± 0.2	30	1.5	8.5	ns
time			Table 12.7.1, Fig. 12.8.2, Table 12.8.1	2.7	50	1.5	7.6	
			Table 12.0.1	3.3 ± 0.3	50	1.5	6.6	
Output skew	t _{osLH} ,t _{osHL}	(Note 1)	_	2.5 ± 0.2	30	_	_	ns
				2.7	50	_	_	
				3.3 ± 0.3	50		1.0	

Note: Operating Range spec of T_{opr} = -40 °C to 125 °C is applicable only for the products which manufactured after April 2020.

Note 1: Parameter guaranteed by design. $(t_{osLH} = |t_{PLH}m-t_{PLH}n|, t_{osHL} = |t_{PHL}m-t_{PHL}n|)$

12.5. Dynamic Switching Characteristics (Unless otherwise specified, T_a = 25°C, Input: t_r = t_f = 2.5 ns, R_L = 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	V _{IH} = 2.5 V, V _{IL} = 0 V, C _L = 30 pF	2.5	0.6	V
		V _{IH} = 3.3 V, V _{IL} = 0 V, C _L = 50 pF	3.3	0.8	
Quiet output minimum dynamic V _{OL}	V _{OLV}	V _{IH} = 2.5 V, V _{IL} = 0 V, C _L = 30 pF	2.5	0.6	V
		V _{IH} = 3.3 V, V _{IL} = 0 V, C _L = 50 pF	3.3	0.8	

12.6. Capacitive Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}		_	3.3	7	pF
Bus I/O capacitance	C _{I/O}		_	3.3	8	pF
Power dissipation capacitance	C _{PD}	(Note 1)	f _{IN} = 10 MHz	3.3	25	pF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{|N} + I_{CC}/16 \text{ (per bit)}$



12.7. AC Test Circuit

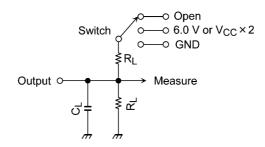


Table 12.7.1 Parameter for AC Test Circuit

Parameter	Switch	Test Condition
t _{PLH} , t _{PHL}	OPEN	_
t _{PLZ} , t _{PZL}	6.0 V	V_{CC} = 3.3 ± 0.3 V
	V _{CC} × 2	V_{CC} = 2.5 \pm 0.2 V
t _{PHZ} , t _{PZH}	GND	_



12.8. AC Waveform

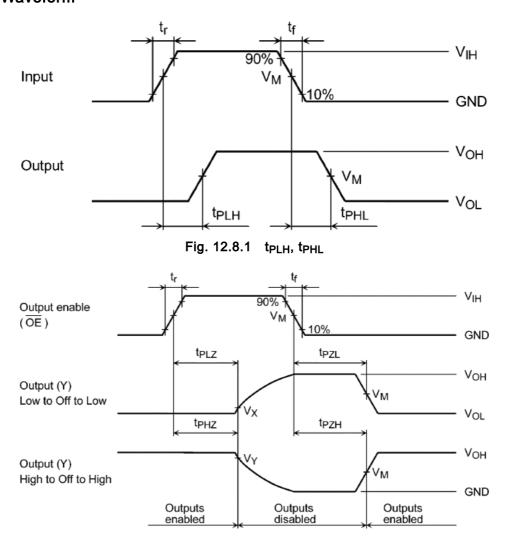


Fig. 12.8.2 t_{PLZ}, t_{PHZ}, t_{PZL}, t_{PZH}

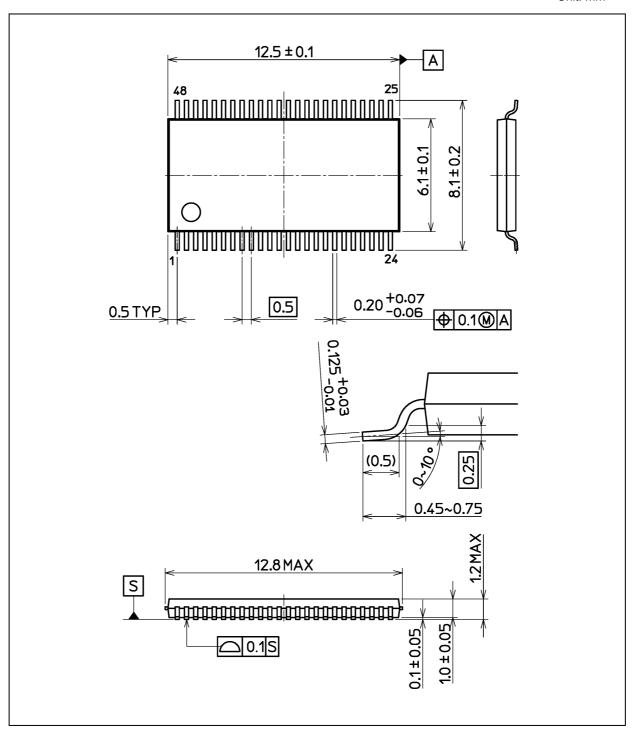
Table 12.8.1 AC Waveform Symbols

Symbol	V_{CC} = 3.3 ± 0.3 V	V _{CC} = 2.7 V	V_{CC} = 2.5 ± 0.2 V
V _{IH}	2.7 V	2.7 V	V _{CC}
V_{M}	1.5 V	1.5 V	V _{CC} /2
V _X	V _{OL} + 0.3 V	V _{OL} + 0.3 V	V _{OL} + 0.15 V
V_{Y}	V _{OH} - 0.3 V	V _{OH} - 0.3 V	V _{OH} - 0.15 V
C _L	50 pF	30 pF	30 pF
R_L	500 Ω	500 Ω	500 Ω



Package Dimensions

Unit: mm



Weight: 0.25 g (typ.)

Package Name(s)
Nickname: TSSOP48



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