

TLCS-900 Family TLCS-900/L1 Series

16-bit / 100-pin

TMP91CW12AFG TMP91CY22FG

**High-speed and high-functionality 16-bit microcontroller
capable of low-voltage and low-power operation**

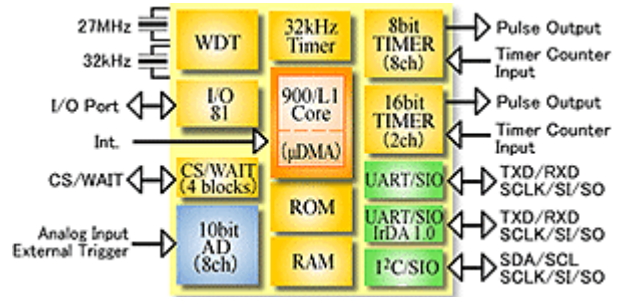
Features

TLCS-900/L1 CPU Core

- ▶ Operating voltage
1.8 to 3.6 V
- ▶ Minimum instruction execution time
148 ns (at 27 MHz/ 2.7 to 3.6 V)

Built-in Functions

- ▶ 10-bit AD converter : 8 channels
- ▶ UART/SIO : 1 channel
- ▶ UART/SIO/IrDA ver1.0 (115.2 kbps) : 1 channel
- ▶ I²C/SIO : 1 channel
- ▶ 8-bit timer : 8 channels
- ▶ 16-bit timer : 2 channels
- ▶ 32 kHz timer

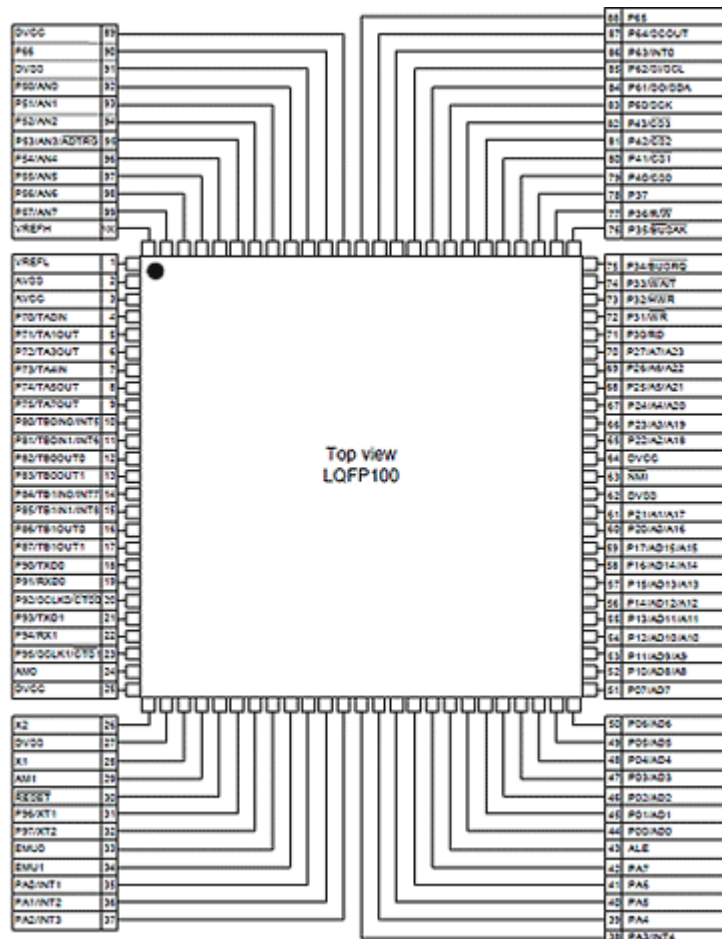


Memory Size

Part number	ROM	RAM
TMP91CW12AFG	128 Kbytes	4 Kbytes
TMP91CY22FG	256 Kbytes	16 Kbytes

Package Information

Pin Assignments



Package name LQFP100-P-1414-0.50F

Software Products

Toshiba Integrated Development Environment	C Compiler	SW96CN0-ZCC: 1 license SW96CN3-ZCC: 10 licenses
	Integrated Development Environment	SW00MN0-ZCC: 1 license SW00MN3-ZCC: 10 licenses The controller comes with a single-seat download license.
Real-Time OS (μITRON 3.0)		SW96RN2-ZCC: Object code can be freely copied SW96RNC-ZCC: Object code can be freely copied. With source code.

Hardware Products Emulator

Controller	BM1040R0B-G
Emulation pod	BM91CW12AF0A-M15

* BM91CW12AF0A-M15 does not support 2V operation.

* As to hardware products, additional accessories are also needed.

» For further information about Toshiba microcomputers and Toshiba microcomputer development systems, please visit <http://www.semicon.toshiba.co.jp/eng/product/micro/index.html>

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