

TLCS-900 Family TLCS-900/H1 Series

32-bit / 100-pin

TMP92FD23AFG TMP92FD23ADFG



32-bit microcontroller with large capacity Flash memory and various serial interfaces

Features

TLCS-900/H1 CPU Core

- Operating voltage 3.0 to 3.6 V
- Minimum instruction execution time 50 ns (at 20 MHz/ 3.0 to 3.6 V internal)
- Internal ROM: 512 Kbytes Internal RAM: 32 Kbytes

Built-in Functions

- ▶ 10-bit AD converter : 12 channels
- ► UART/SIO/HSIO: 1 channel
- ► UART/SIO : 2 channels
- ► I²C/SIO : 2 channels
- ► 8-bit timer : 6 channels
- ► 16-bit timer : 2 channels
- Key-on wake-up : 8 channels
- Program patch logic : 8 banks

10MHz € PLL 32kHz € WDT	RTC	8bit TIMER (6ch) Pulse Output Timer Counter Input
Analog Input 10bit AD	ROM	16bit Pulse Output TIMER (2ch) Pulse Output Timer Counter Input
External Trigger (E2ch)	900/H1 Core (µDMA)	UART SIO/ HSIO HSIO (ich) TXD/RXD SCLK/SI/SO HSCLK/HSSD/HSSO
I/O Port 10 10 84	RAM	UART /SIO (2ch) → TXD/RXD SCLK/SI/SO
CS/WAIT	KAM	(2ch) SDA/SCL SCLK/SI/SO

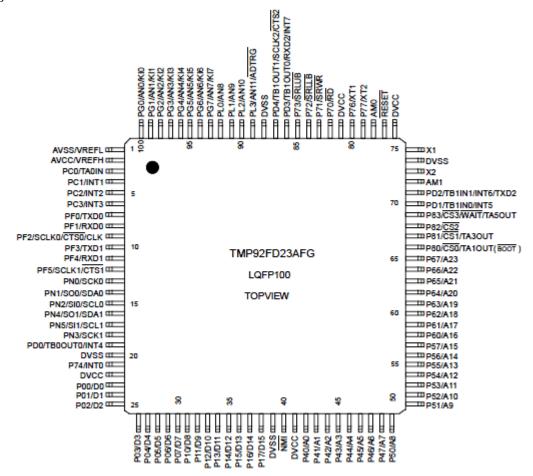
Product Lineup

Part number	ROM	RAM
TMP92CD23AFG	512 Kbytes	32 Kbytes
TMP92CD23ADFG	512 Kbytes	32 Kbytes

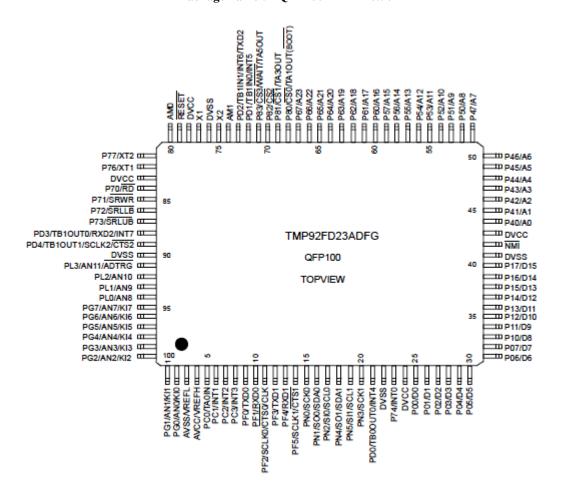
^{*} This product uses the SuperFlash® technology under license of Silicon Storage Technology, Inc. Super Flash® is a registered trademark of Silicon Storage Technology, Inc.

Package Information

Pin Assignments



Package name: LQFP100-P-1414-0.50F



Package name: QFP100-P-1420-0.65A

Development Systems

Software Products

	C Compiler	SW96CN0-ZCC: 1 license SW96CN3-ZCC: 10 licenses
Toshiba Integrated Development Environment	Integrated Development Environment	SW00MN0-ZCC: 1 license SW00MN3-ZCC: 10 licenses The emulator comes with a single-seat download license.
Real-Time OS (μΙΤRON 3.0)		SW96RN2-ZCC: Object code can be freely copied SW96RNC-ZCC: Object code can be freely copied. With source code.

Hardware Products Emulator

In-Circuit Emulator HW92ES230AG

- * As to hardware products, additional accessories are also needed.
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